

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10560930
<b>Filing Date:</b>	15-Dec-2005
<b>Title of Invention:</b>	Logic circuit with restrained leak current to differential circuit
<b>First Named Inventor/Applicant Name:</b>	Yasushi Amamiya
<b>Filer:</b>	Justin M. Sobaje/Ruthie Vallejo
<b>Attorney Docket Number:</b>	040373-0367

Filed as Large Entity

### U.S. National Stage under 35 USC 371 Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
Independent claims in excess of 3	1614	2	210	420
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 1 month with \$0 paid	1251	1	120	120
Miscellaneous:				
Total in USD (\$)				540